

PTO/SB/21 - MODIFIED
09/2004

2631

**TRANSMITTAL
FORM***to be used for all correspondence after initial filing*

Application Number 09/920,241

Filing Date 08/01/2001

First Named Inventor Pierte ROO

Group Art Unit 2631

Examiner Name M. Ghayour

Total number of pages in this submission including transmittal

5

Attorney Docket Number MP0088

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached - Check	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment / Reply	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Other Enclosure(s) (identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	
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<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

REMARKS

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name KATTEN MUCHIN ROSENMAN, LLP

Signature

Printed Name Andrew J. Bateman

Reg. No. 45,573

Date: 07/25/2005

CERTIFICATE OF FACSIMILE TRANSMISSION OR MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO via fax no. (571) 273-8300 or is being deposited with the US Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature

Typed or Printed Name

Date:



MP0088

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
Pierte ROO)	Examiner: M. Ghayour
Application No.: 09/920,241)	Group Art Unit: 2631
Filed: August 1, 2001)	
For: APPARATUS AND METHOD FOR)	
CONVERTING SINGLE-ENDED SIGNALS TO A)	
DIFFERENTIAL SIGNAL, AND TRANSCEIVER)	
EMPLOYING SAME)	Date: July 25, 2005

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and the requirements of M.P.E.P. § 2001.06(c), and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449. It is respectfully noted that Applicants do not have copies of certain literature and non-U.S. patent documents listed on the enclosed PTO-1449 forms. Applicants will provide copies of such to the Patent Office as soon as possible. If the Examiner has not received such copies at the time of consideration of the IDS, the Examiner is respectfully requested to contact the Applicants' undersigned attorney.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

MP0088

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No fee is necessary for the submission of this Information Disclosure Statement.

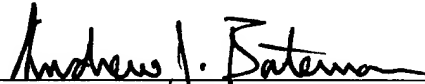
Should the Examiner have any questions, Applicants' undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

Respectfully submitted,

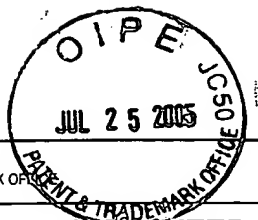
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FORM PTO 1449 MODIFIED
U.S. PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO.

APPLICATION NO.

MP0088

09/920,241

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Pierre ROO

DATE SUBMITTED TO USPTO: July 25, 2005

FILING DATE

GROUP

08/01/2001

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FOREIGN PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT

OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)

	LIBERALI ET AL., "Progress in High-Speed and High -Resolution CMOS Data Converters", September 12-14, 1995, pages 19-28
	SEDRA et al., "Micro-Electronic Circuits", 1982, pages 95-97 and 243-247
	DP83220 CDL TM Twisted Pair FDDI Transceiver Device", October 1992
	SU ET AL., "A CMOS Oversampling D/A Converter with a Current-Mode Semidigital Reconstruction Filter", December 1993, pages 1224-1233
	GOLDBERG, Gigabit Ethernet PHY Chip Sets LAN Speed Record for Copper Story" 6 pages
	MIKI ET AL., "An 80-MHz 8-bit CMOS D/A Converter", December 1986, pages 983-988
	LETHAM ET AL., "A high-performance CMOS 70-Mhzpalette/DAC", December 1987, pages 1041-1047
	NAKAMURA ET AL., "A 10-b 70-MS/s CMOS D/A/ converter", April 1991, pages 637-642
	TAKAKURA ET AL., "A10 bit 80 MHz glitchless CMOS D/A/ converter", May 1991, pages 26.5.1-26.5.4
	FOURNIER ET AL., "A 130-MHz 8-b CMOS video DAC for HDTV applications", July 1991, pages 1073-1077
	REYNOLDS, "A 320 MHz CMOS triple 8b DAC with on-chip PLL and hardware cursor", February 1994, pages 50-51
	CHIN ET AL., "A 10-b 125 MHz CMOS digital-to-analog (DAC) with threshold-voltage compensated current sources", November 1994, pages 1374-1380
	WU ET AL., "A low glitch 10-bit 75-MHz CMOS video D/A converter, January 1995, pages 68-72
	The Authoritative Dictionary of IEEE Standards Stems 7th Edition, page 280
	GRAY ET AL., "Analysis and Design of Analog Integrated Circuits, 1997

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,254,994	10/19/1993	Takakura et al.			
	5,585,802	12/17/1996	Cabler et al.			
	5,821,892	10/13/1998	Smith			
	3,500,215	03/10/1970	Leuthold et al.			
	3,521,170	11/27/1970	Leuthold et al.			
	3,793,589	02/19/1974	Puckette			
	4,393,370	07/12/1993	Hareyama			
	6,476,476 B2	11/05/2002	Viswanathan			
	4,947,171	08/07/1990	Pfeifer			
	6,309,077 B1	10/30/2001	Saif et al.			
	6,594,304 B2	07/15/2003	Chan			
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	6,535,987 B1	03/18/2003	Ferrant			
	6,633,178 B2	10/14/2003	Wilcox et al.			

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